NR

Code No: A7710

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.Tech I Semester Examinations, March/April-2011
EMBEDDED SOFTWARE DESIGN
(EMBEEDED SYSTEMS AND VLSI DESIGN)

Time: 3hours   **Max. Marks: 60**

Answer any five questions
All questions carry equal marks
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1 a) With neat sketches explain the Pentium Memory Management unit.   (7)
  b) Briefly explain the New Pentium instructions.   (5)

2. Explain Embedded Design life cycle.   (12)

3. Write notes on:
    a) RTOS availability.   (6)
    b) Tool chain availability.   (6)

4. Explain the basic tool set of embedded system.   (12)

5 a) Explain about Back ground Debug Mode.   (5)
  b) Give the Functional differences between ROM emulator and Logic analyzer.
                                                                            (7)
6. Explain about Embedded Software testing.   (12)

7.a) Explain the concept of overlay memory.   (6)
  b) What is the importance of testing and Describe types of tests.   (6)

8. Explain various Data structures used in Embedded System Software.   (12)

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